

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor: Ishai Nachumovsky
Assignee: Tower Semiconductor, Ltd.
Title: TWO-BIT SPLIT-GATE NON-VOLATILE MEMORY TRANSISTOR
Serial No.: 09/975,049 Group No.: 2814
Filed: 10/10/2001 Examiner: Howard Weiss
Docket No.: TSL-105

Date: November 10, 2003

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Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT IN RESPONSE TO FINAL OFFICE ACTION

Sir:

Applicant submits the following remarks in response to the Final Office Action dated June 10, 2003. A two-month extension of time is enclosed.

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